

# DSP Algorithms and Architecture

Written by Administrator  
Saturday, 07 November 2009 07:45 -

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Subject Code

:

**06EC74**

IA Marks

:

25

No. of Lecture Hrs/ Week

:

04

Exam Hrs

:









### Reference Books:

1. **Digital Signal Processing** – Proffesor E. C., Jervis B. W A practical approach, Pearson-Education, PHI. 2002.
2. **Digital Signal Processors** – B Venkataramani and M Bhaskar TMH, 2002.
3. **Architectures for Digital Signal Processing** – Peter Pirsch John Weily, 2007.