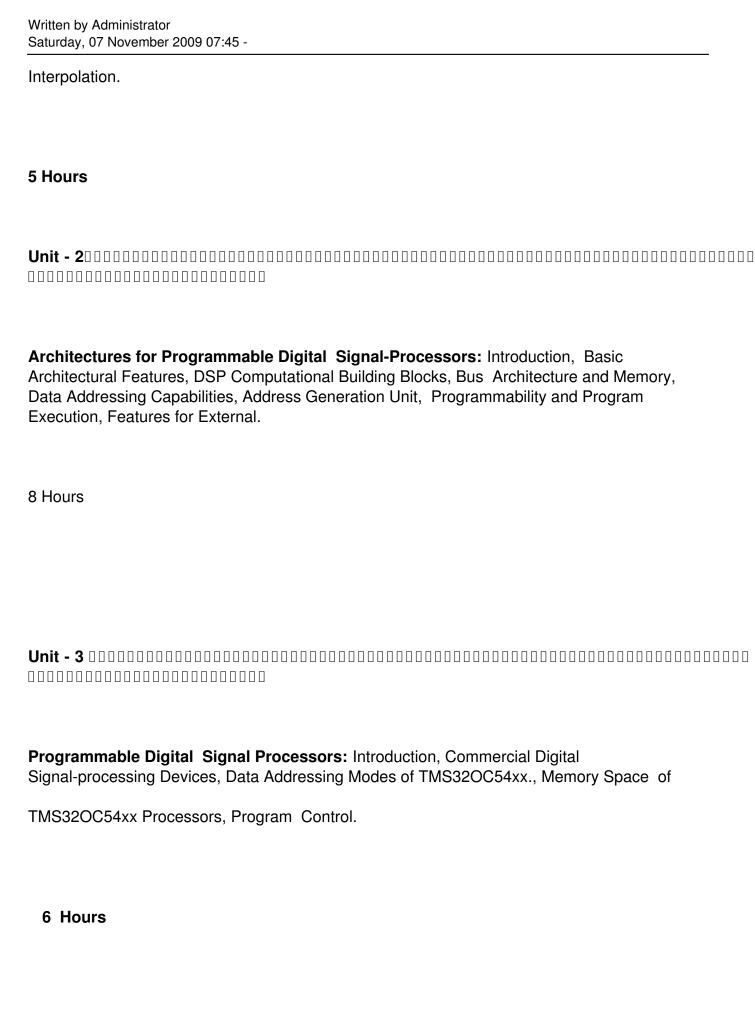
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Written by Administrator
Saturday, 07 November 2009 07:45 -
Subject Code
06EC74 IA Marks
25
No. of Lecture Hrs/ Week
04
Exam Hrs

Written by Administrator Saturday, 07 November 2009 07:45 -
03
Total no. of Lecture Hrs.
52
Exam Marks
100
PART - A
Unit - 1 0000000000000000000000000000000000
Introduction to Digital Cignal Proposalings Introduction A Digital Cignal Proposaling Contact
Introduction to Digital Signal Processing: Introduction, A Digital Signal-Processing System, The Sampling Process, Discrete Time Sequences, Discrete Fourier Transform (DFT) and Fast
Fourier Transform (FFT), Linear Time-Invariant Systems, Digital Filters, Decimation and

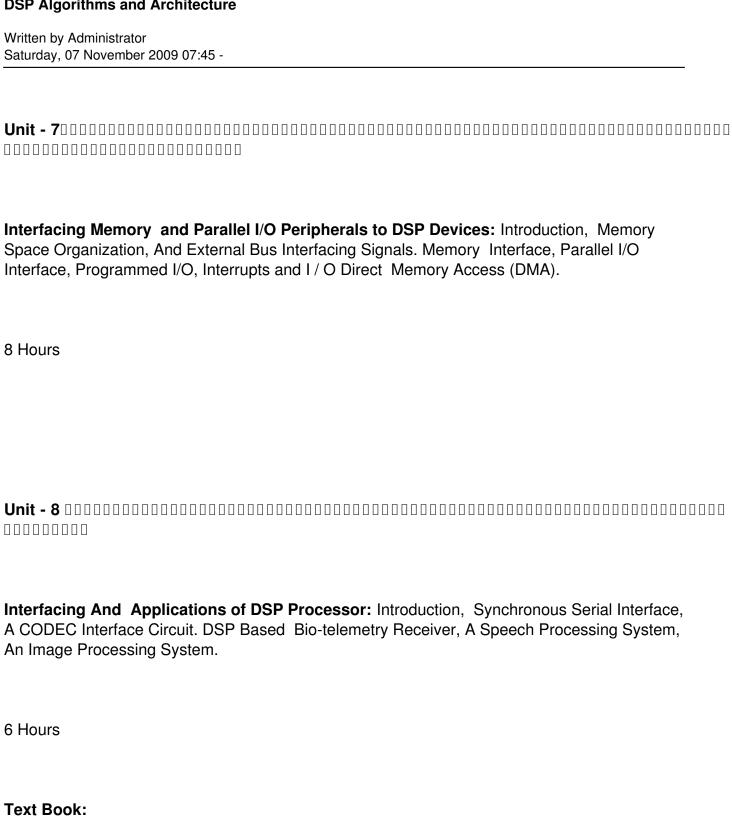


6 Hours

Written by Administrator Saturday, 07 November 2009 07:45 -Detail Study of TMS320C54X & 54xx Instructions and Programming, On-Chip peripherals, TMS32OC54XX Processors, Pipeline Interrupts of Operation of TMS32OC54xx Processor. 7 Hours PART - B Unit - 5 Implementation of Basic DSP Algorithms: Introduction, The Q-notation, FIR Filters, IIR Filters, Interpolation and Decimation Filters(one example in each case). 6 Hours Implementation of FFT Algorithms: Introduction, An FFT Algorithm for DFT Computation, Overflow and Scaling, Bit-Reversed Index Generation & Implementation on the TMS32OC54xx.

1.

4.



Digital Signal Processing – Avatar Singh and S. Srinivasan, Thomson Learning,

200

Written by Administrator Saturday, 07 November 2009 07:45 -

Reference Books:

- 1. **Digital Signal Processing** Ifeachor E. C., Jervis B. W A practical approach, Pearson-Education, /PHI. 2002.
 - 2. **Digital Signal Processors** B Venkataramani and M Bhaskar TMH, 2002.
- 3. **Architectures for Digital Signal Processing** Peter Pirsch John Weily, 2007.