

ELECTIVE – GROUP A

Written by Administrator
Saturday, 07 November 2009 05:33 -

Programming in C++

Subject Code		:
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IA Marks		: 25
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No. of Lecture Hrs/Week		: 04
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Exam Hours		: 03
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Total no. of Lecture Hrs.		: 52
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Exam Marks		: 100
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References books:

1. **Satellite Communications**, Timothy Pratt, Charles Bostian and Jeremy Allnutt, 2nd Edition, John Wiley & Sons, 2003.

2. **Satellite Communication Systems Engineering**, W. L. Pitchand, H. L. Snyderhoud, R. A. Nelson, 2nd Ed., Pearson Education., 2007.

Random Processes

Subject Code		:
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IA Marks		: 25
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No. of Lecture Hrs/Week		: 04
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Text Book:

1. **Probability and random processes: application to Signal processing and communication** - S L Miller and D C Childers: Academic Press / Elsevier 2004

Reference Books:

1. **Probability, Random variables and stochastic processes** - A. Papoullis and S U Pillai: McGraw Hill
2002
2. **Probability, Random variables and Random signal principles** - Peyton Z Peebles: TMH
4th
Edition 2007
3. **Probability, random processes and applications** - H Stark and Woods: PHI
2001

Adaptive Signal Processing

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Subject Code		:
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IA Marks		: 25
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No. of Lecture Hrs/Week		: 04
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Exam Hours		: 03
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Total no. of Lecture Hrs.		: 52
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Exam Marks		: 100
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PART - A

Unit - 1

Adaptive Systems: Definition and characteristics, Areas of application, General properties, Open-and closed-loop adaptation, Applications of closed-loop adaptation, Example of an adaptive system.

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Reference Books:

1. **Adaptive filter Theory**, Simon Haykin, 4e, Pearson Education Asia, 2002
2. **Theory and Design of Adaptive Filters**, Jophn R. Treichler C. Richard Johnson, Jr. and Michael G. Larimore, Pearson education / PHI 2002.

Low Power VLSI Design

Subject Code		:
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IA Marks		: 25
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No. of Lecture Hrs/Week		: 04
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MODERN CONTROL THEORY

Subject Code		:
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IA Marks		: 25
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No. of Lecture Hrs/Week		: 04
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Exam Hours		: 03
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Total no. of Lecture Hrs.		: 52
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Exam Marks		: 100
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Reference Books:

1. **Modern Control System** – Richard Dorf & Robert Bishop Pearson Education/ PHI.
2. **Modern Control Engineering** - K. Ogata Pearson Education / PHI

Digital Systems Design Using VHDL

Subject Code		:
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IA Marks		: 25
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No. of Lecture Hrs/Week		: 04
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Exam Hours		: 03
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Total no. of Lecture Hrs.		: 52
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6 Hours

Text Book:

1. **Digital Systems Design using VHDL**, Charles H. Roth. Jr., Thomson Learning, Inc, 9th reprint, 2006.

Reference Books:

1. **Fundamentals of Digital Logic Design with VHDL**, Stephen Brwon & Zvonko Vranesic, Tata McGraw-Hill, New Delhi, 2nd Ed., 2007
2. **Digital System Design with VHDL**, Mark Zwolinski, 2 Ed, Pearson Education., 2004
3. **Digital Electronics and Design with VHDL** - Volnei A Pedroni, Elsevier Science, 2009