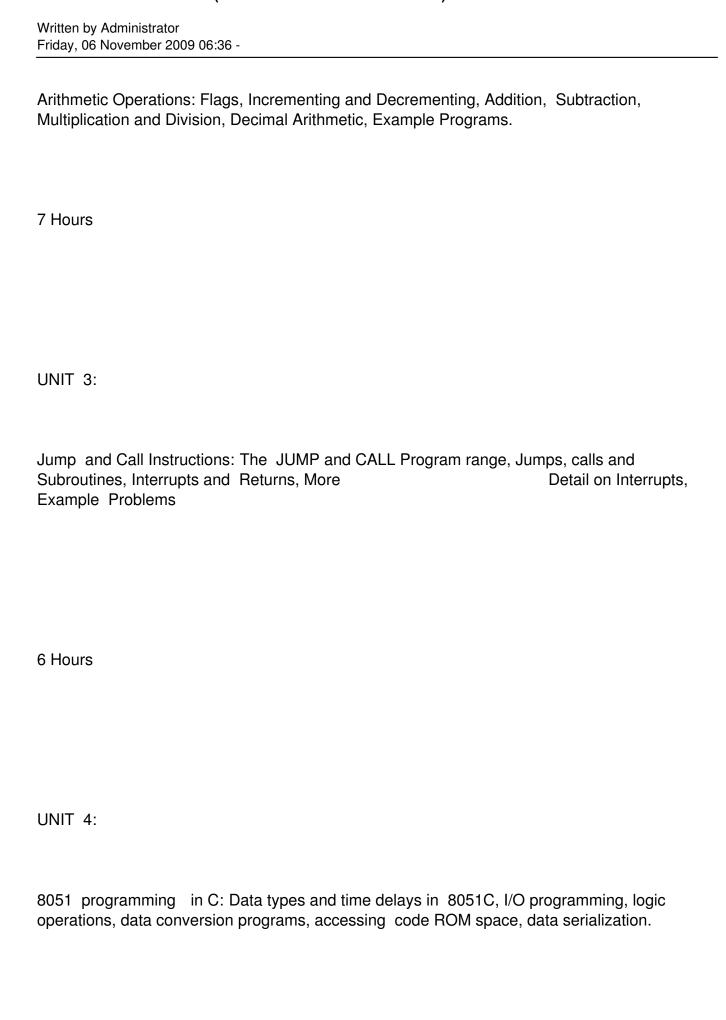
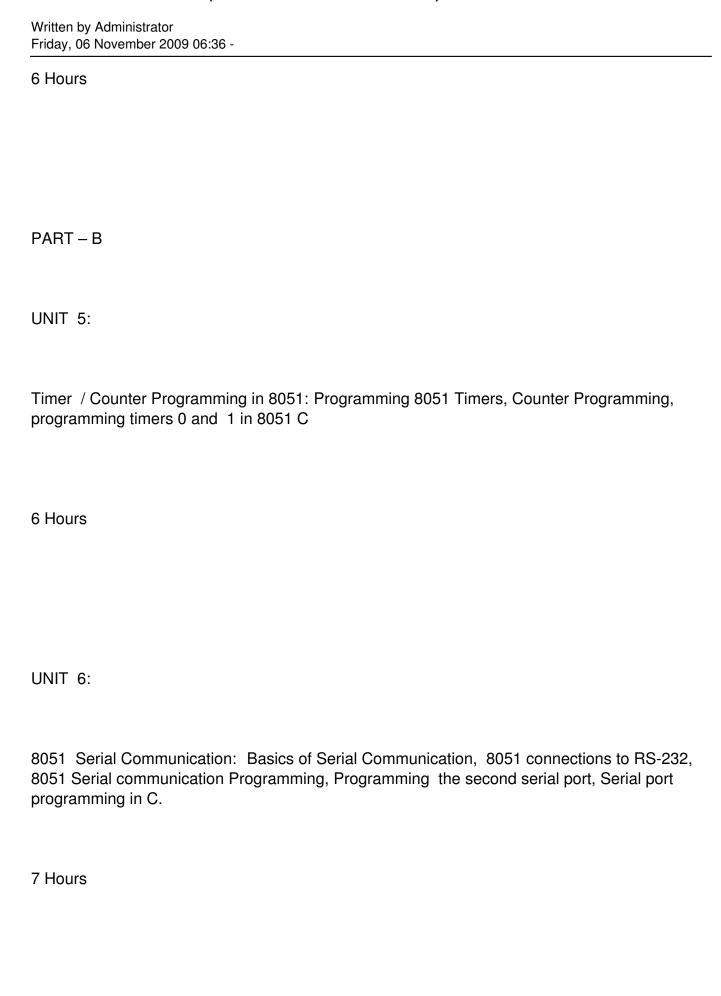
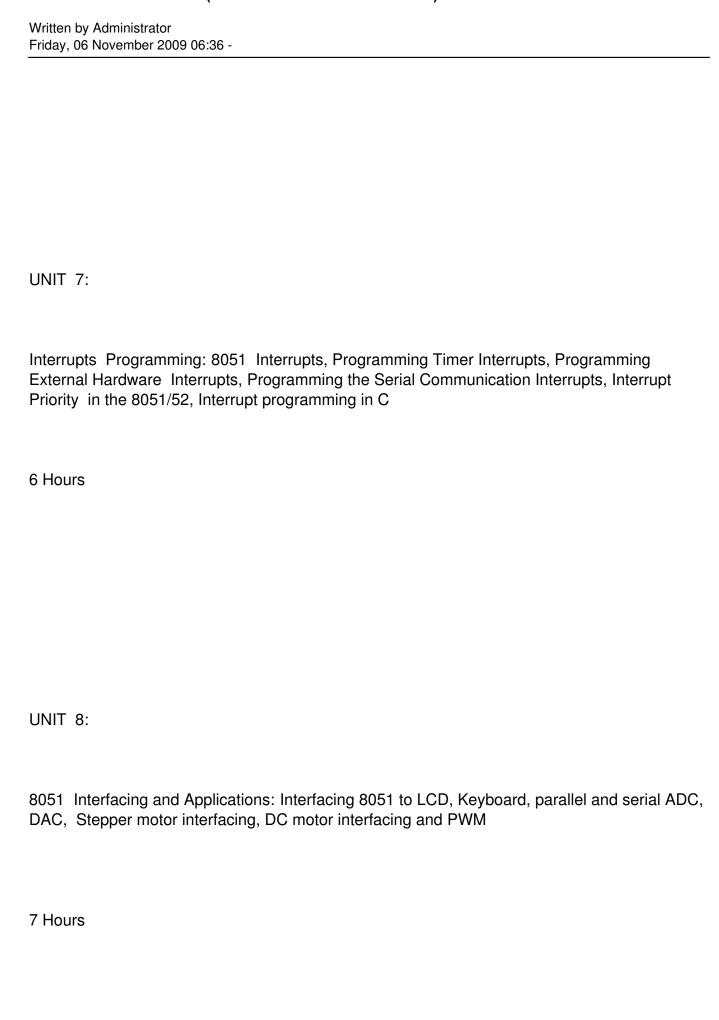
Written by Administrator Friday, 06 November 2009 06:36 -
Sub Code
06ES42
IA Marks
25
Hrs/ Week
04
Exam Hours

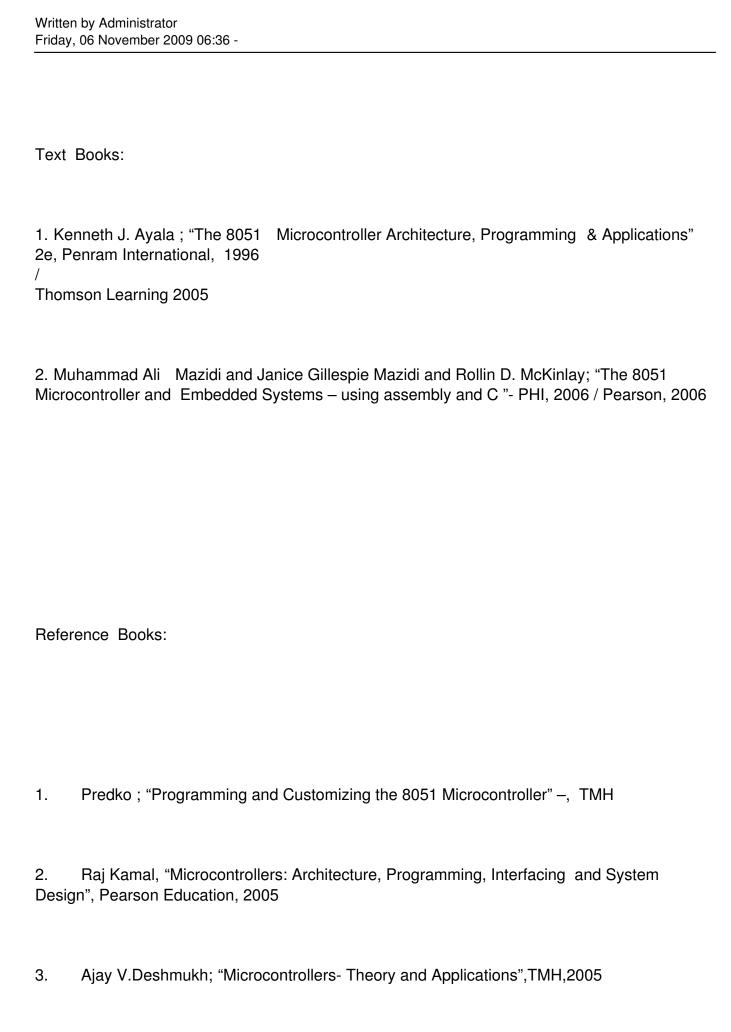
Written by Administrator Friday, 06 November 2009 06:36 -
03
Total Hrs.
52
Exam Marks
100

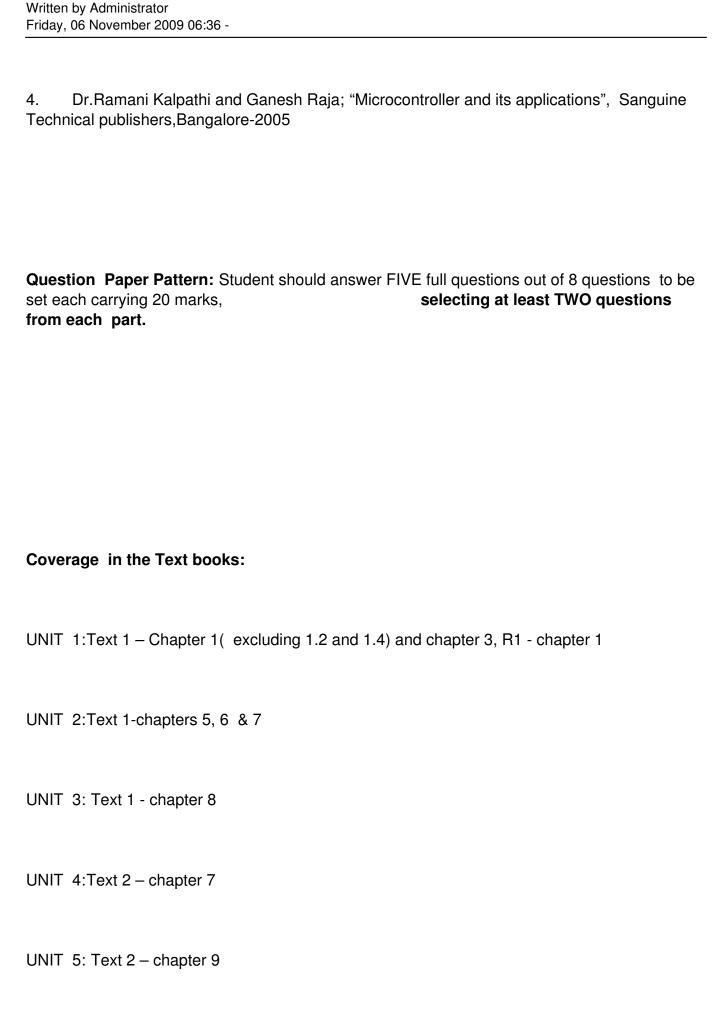
Written by Administrator Friday, 06 November 2009 06:36 -
PART – A
UNIT 1:
Microprocessors and microcontroller. Introduction, Microprocessors and Microcontrollers, A Microprocessors survey. RISC & CISC CPU Architectures, Harvard & Von-Neumann CPU architecture.
The 8051 Architecture: Introduction, 8051 Microcontroller Hardware, Input / Output Pins, Ports
and Circuits
External Memory, Counter and Timers, Serial Data Input / Output,
Interrupts.
7 Hours
UNIT 2:
Addressing Modes and Operations: Introduction, Addressing modes, External data Moves, Code Memory, Read Only Data Moves / Indexed Addressing mode, PUSH and POP Opcodes, Data exchanges, Example Programs; Byte level logical Operations, Bit level Logical Operations, Rotate and Swap Operations, Example Programs.











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UNIT 6: Text 2 - chapter 10

UNIT 7: Text 2 - chapter 11

UNIT 8: Text 2 - chapter 12, chapter 13(13.1&13.2), chapter 17 (except 17.1)